

Advanced Technical Data

Features

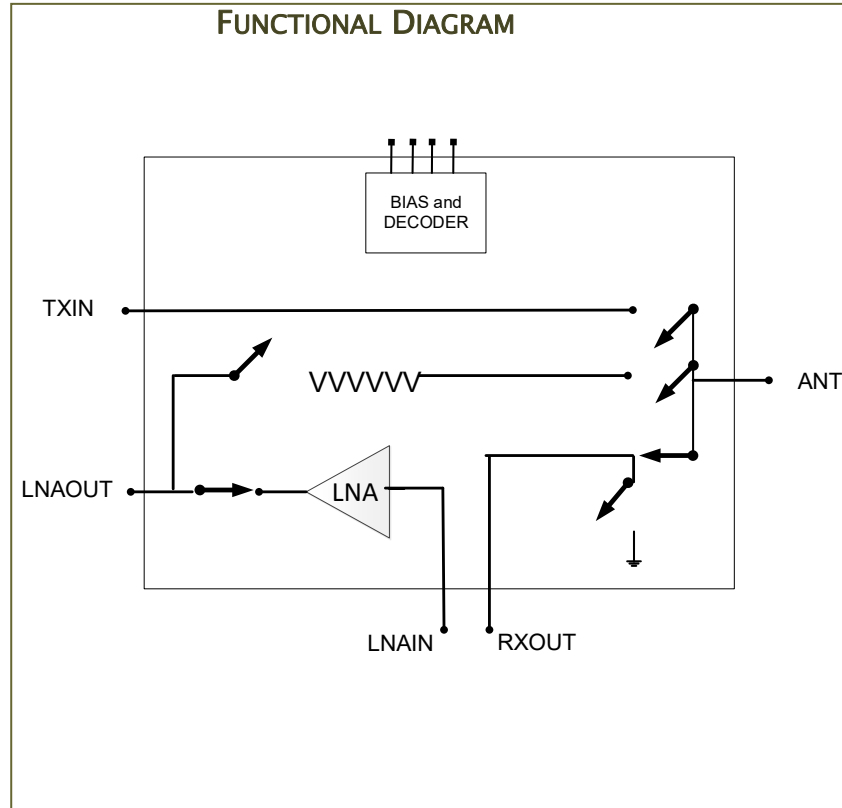
- ☑ CMOS SOI Switch and LNA with Bypass for 802.11 applications for Low Band ,Mid Band and High Band.
- ☑ Designed for 802.11 applications and 5GHz ISM radio applications.
- ☑ Integrated decoder with Range extender pin for configurable IP3. IP3 is programmable from +5dBm IIP3 to +15dBm IIP3
- ☑ ESD protection on all ports.
- ☑ Receive Noise Figure of 1.8dB
- ☑ Receive Gain of 13.5dB and -8dB in bypass mode
- ☑ Operates from a single supply with 2.3V to 5.5V operating range.
- ☑ Small QFN Package 2.3 x 2.3 mm QFN.
- ☑ RohS compliant.

Description

The FE123601 is a single chip CMOS SOI Switch and Low Noise Amplifiers with bypass mode for 5GHz WiFi and ISM applications.

It integrates a decoder and a range extender pin for configurable IP3 for the receive chain. The REX pin is programmable and can set the IIP3 to either +5dBm or +15dBm. The receive Noise Figure is 1.8dB. The receive gain is 13.5dB and in bypass mode is 8dB. The device offers the ability to add custom filtering between the switch and LNA blocks.

FUNCTIONAL DIAGRAM



The device operates from a single supply with a large operating range from 2.3V up to 5.5V.

The FE123601 integrates ESD circuitry on all ports and achieves greater than 2kV HBM protection. It is fabricated using a high performance CMOS SOI process optimized for RF front-end applications.

The device will be available in a 2.3 x 2.3 mm STQFN-16LD package (MSL1) and will be RoHS compliant to EU Directive 2002/95/EC.

Further Information

Please contact sales@ferfics.com